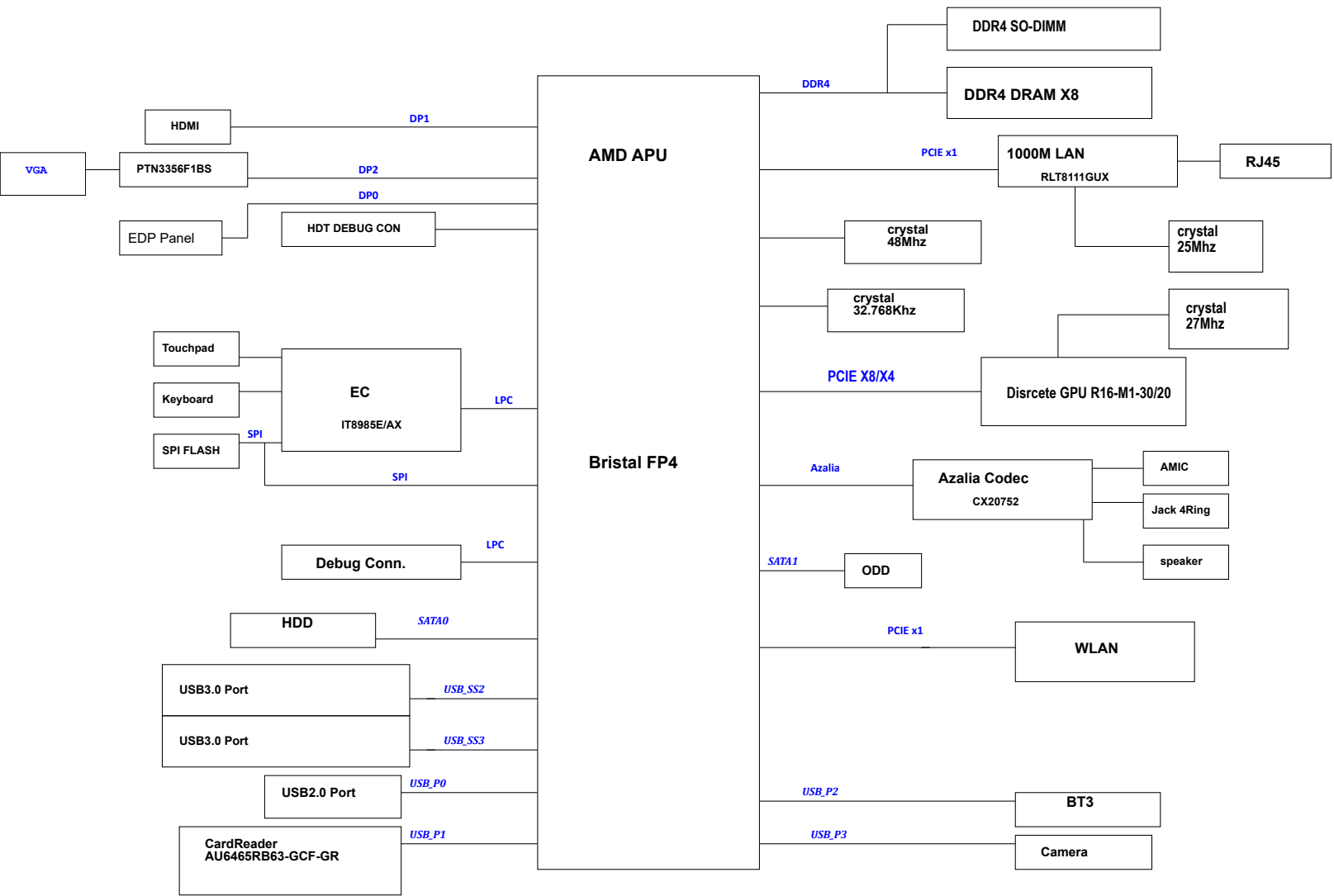
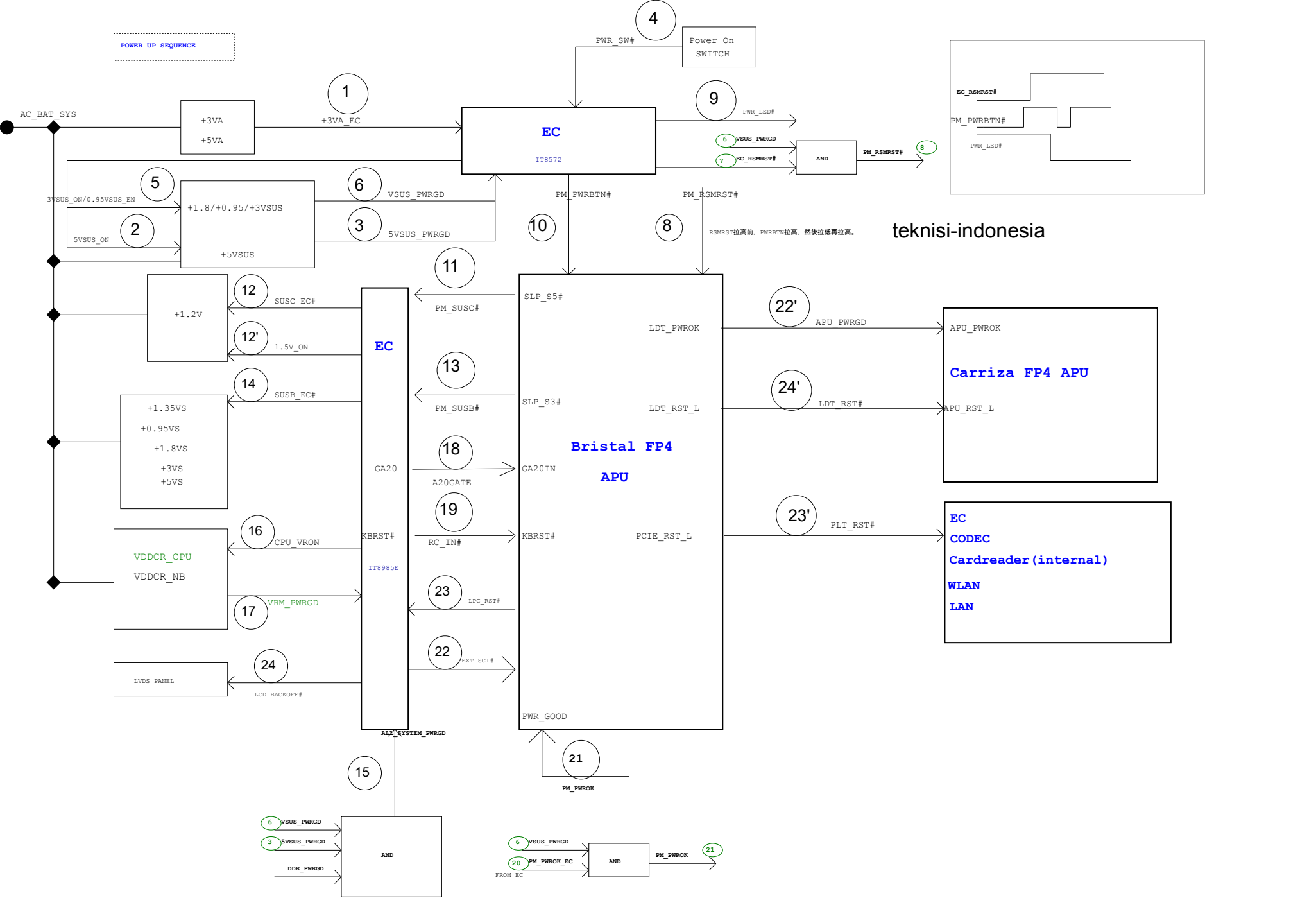


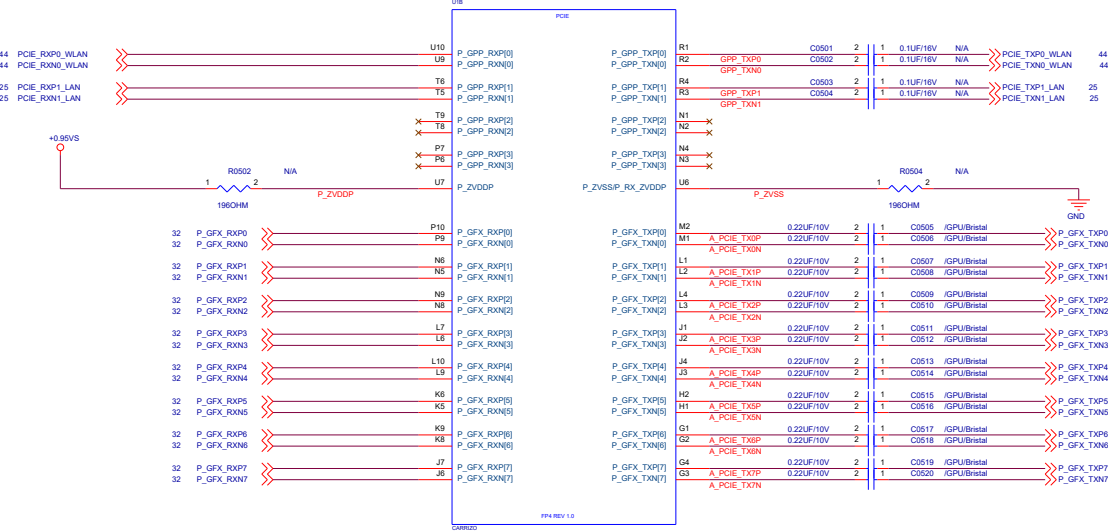
X555Q_B Schematic R1.0



BOM PN	CPU	DRAM	VRAM	adapter w

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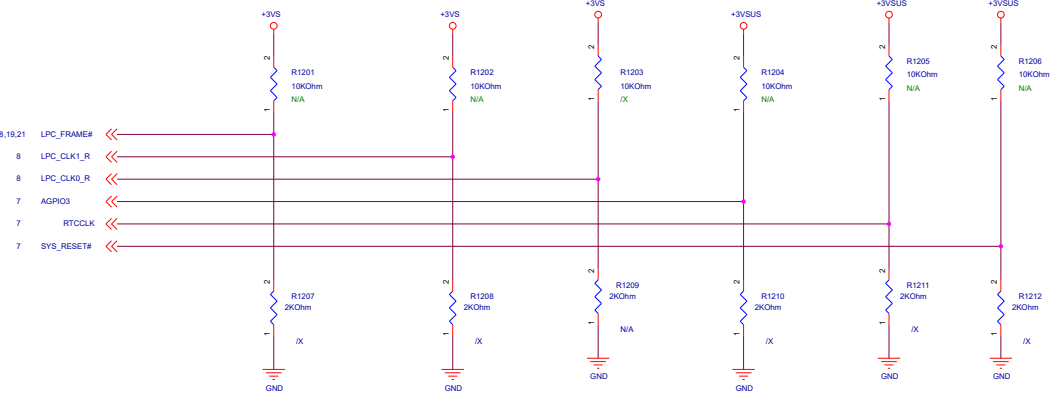




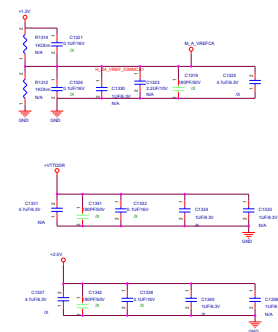
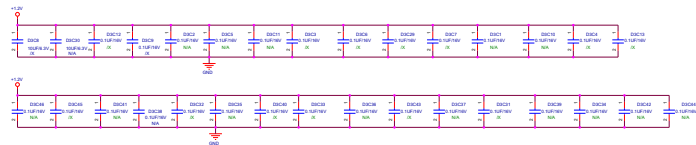
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A_PCIE_TX1P	C0523	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXP1	32
A_PCIE_TX1N	C0524	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXN1	32
A_PCIE_TX2P	C0525	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXP2	32
A_PCIE_TX2N	C0526	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXN2	32
A_PCIE_TX3P	C0527	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXP3	32
A_PCIE_TX3N	C0528	2	1	0.1uF/16V	/GPU/Sony	P_GFX_TXN3	32

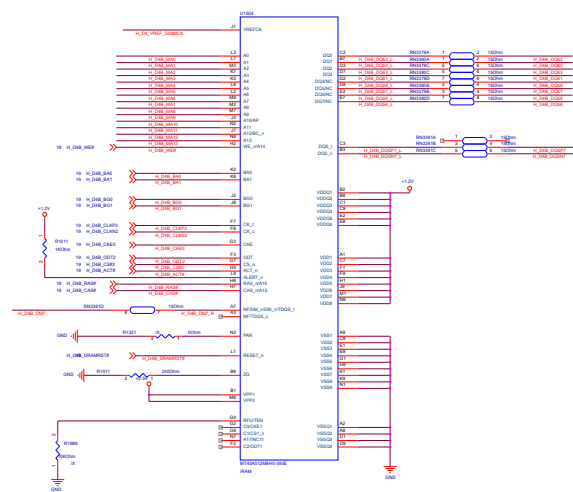
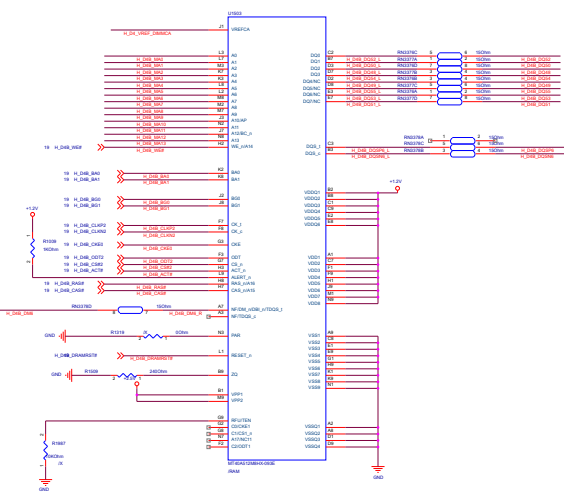
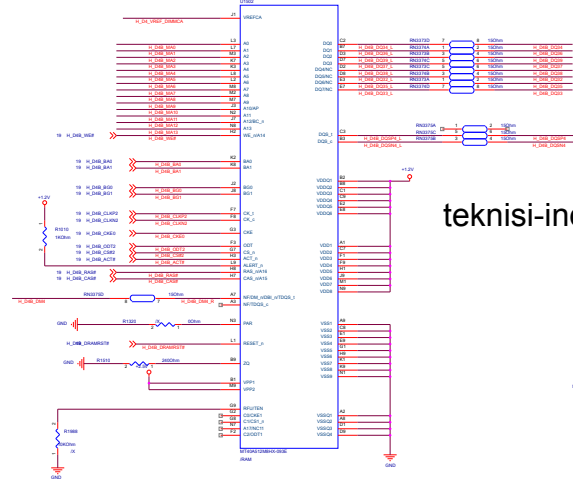






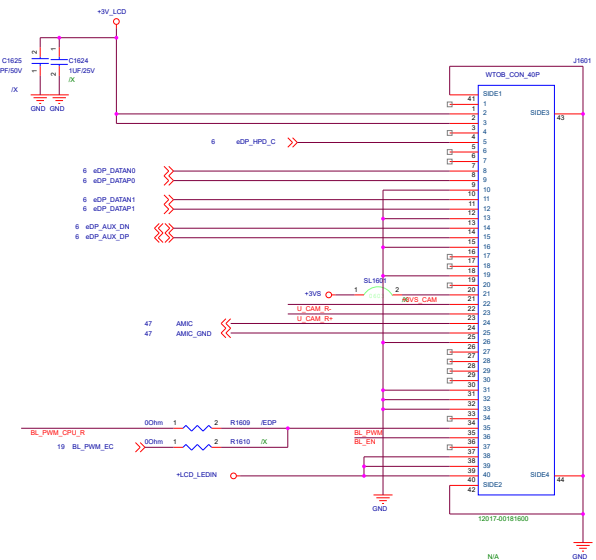
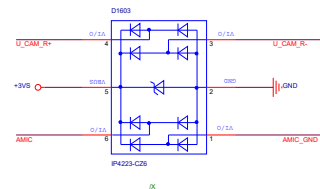
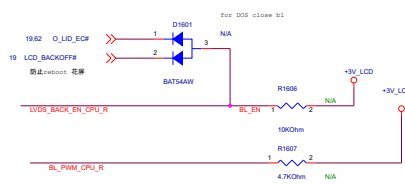
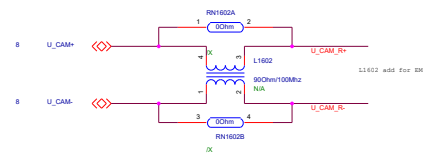
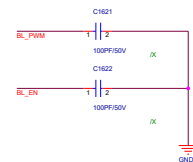
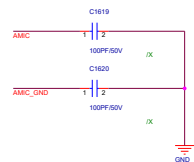
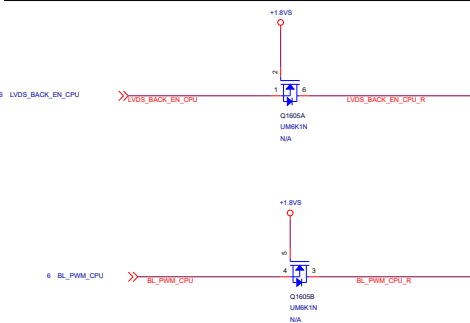
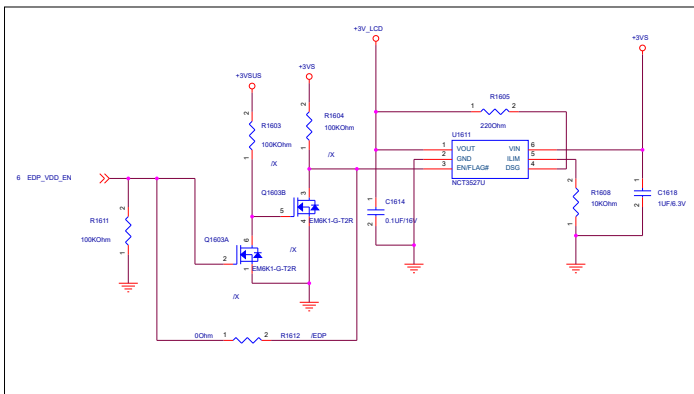
	LPC_FRAME#	LPC_CLK1_R	LPC_CLK0_R	AGPIO3	RTCCLK	SYS_RESET#
PULL HIGH	SPI ROM DEFAULT	configured for internal clock generator DEFAULT	Boot Fail Timer Enabled	Enhanced reset logic (CZ only) DEFAULT	RTC coin battery is implemented DEFAULT	Normal powerup/reset timing DEFAULT
PULL LOW	LPC ROM	Reserved	Boot Fail Timer Disabled DEFAULT	Traditional reset logic	RTC coin battery is not implemented	Reserved

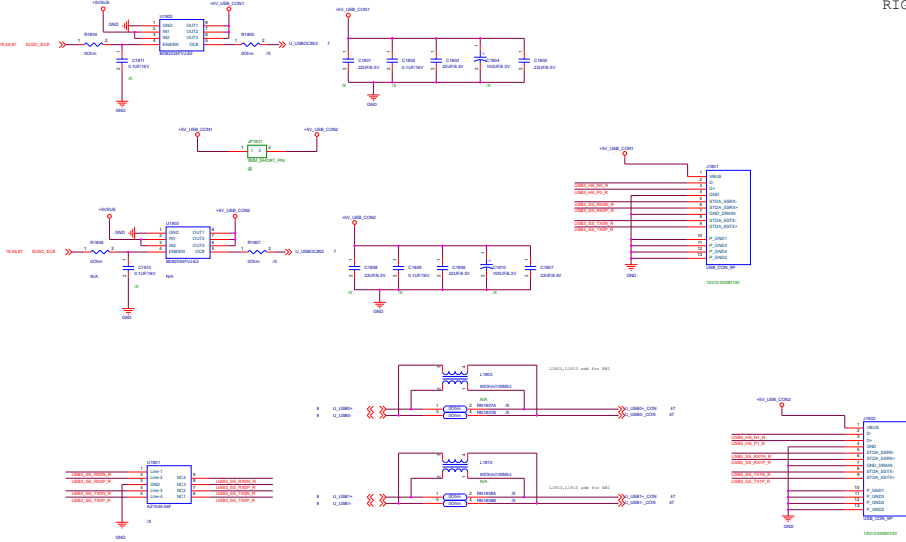




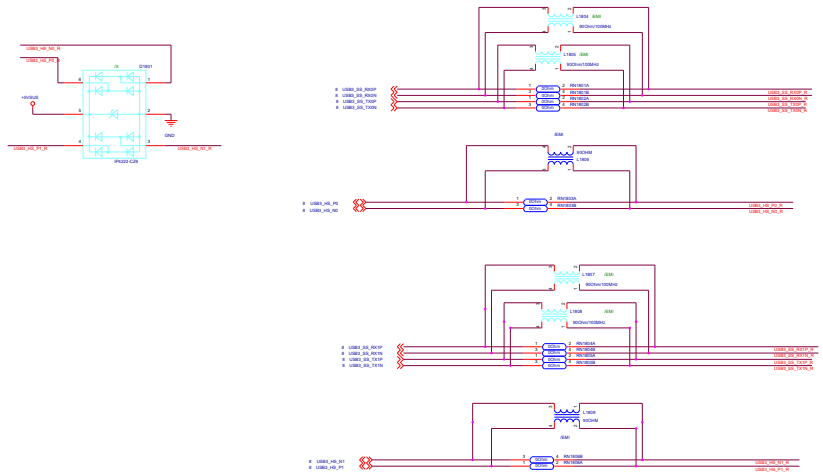
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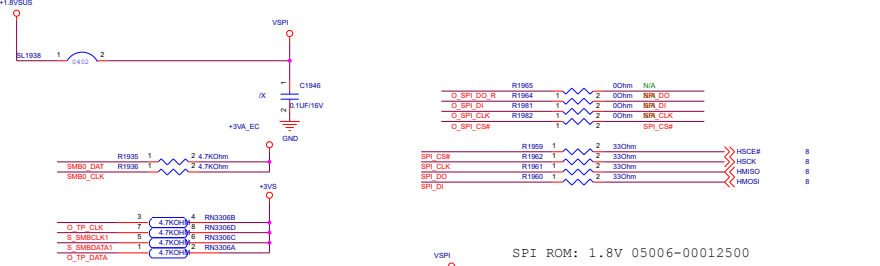
Reserved for LCD soft-start



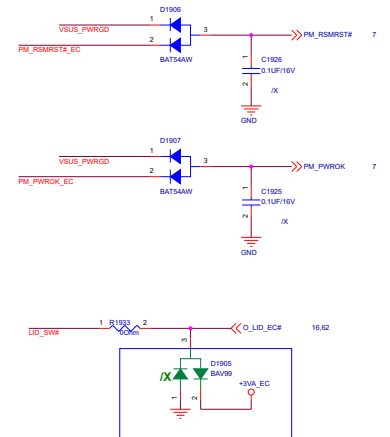
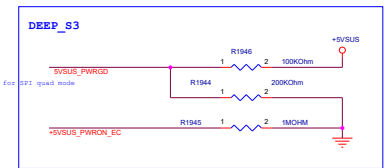


USB3.0/USB 2.0
ESD-Protection

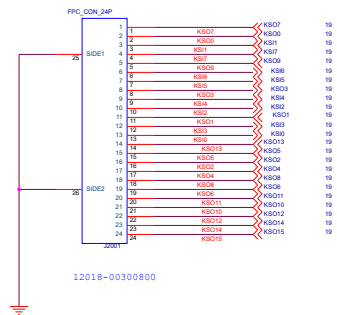




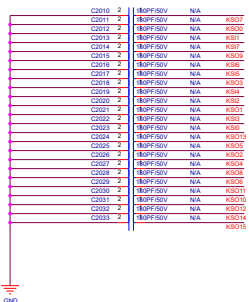
SPI ROM: 1.8V 05006-00012500



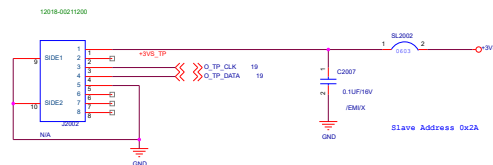
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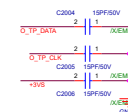
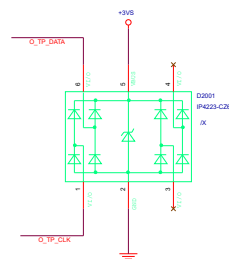
Reserved for EMI



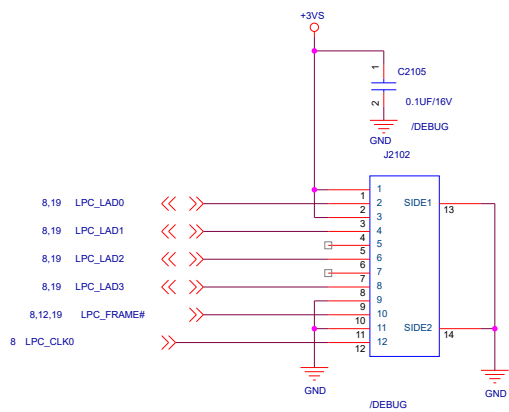
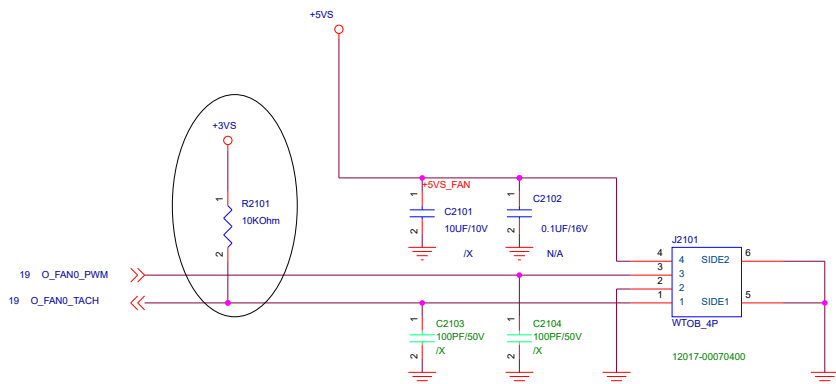
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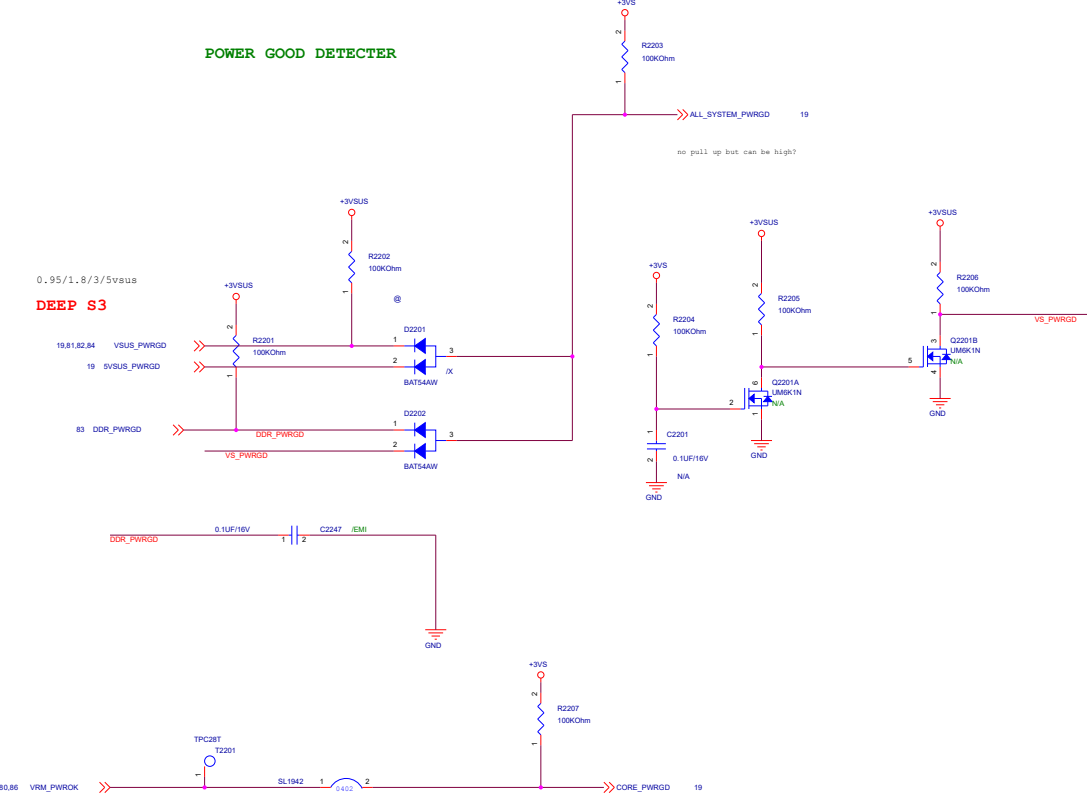
Reserved for EMI



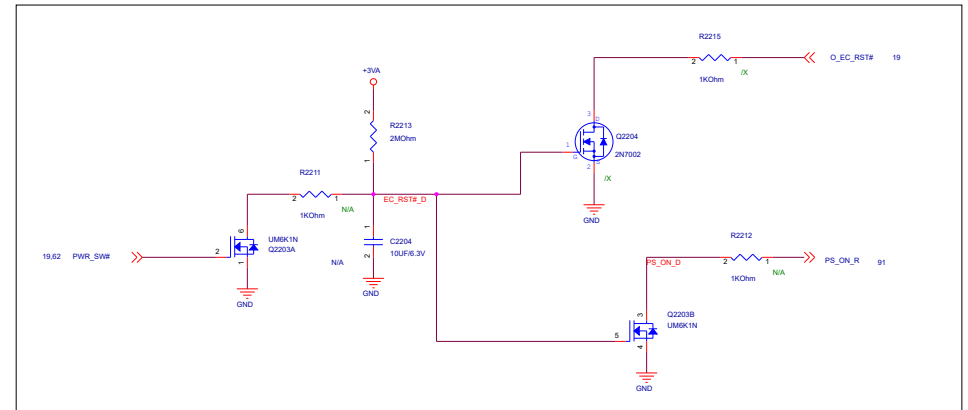
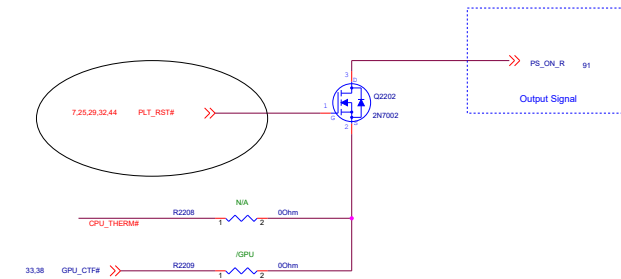
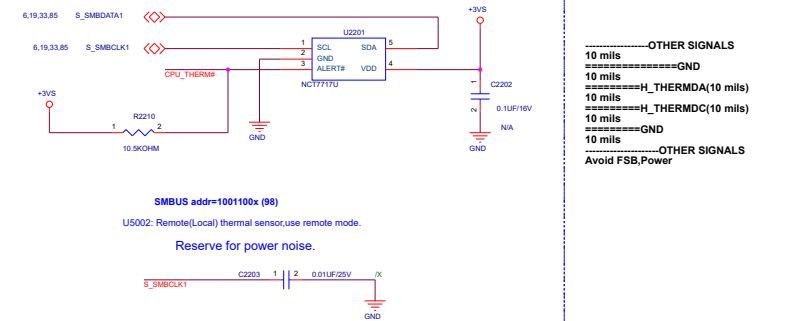
DC FAN Control



DEEP S3



CPU Thermal Sensor

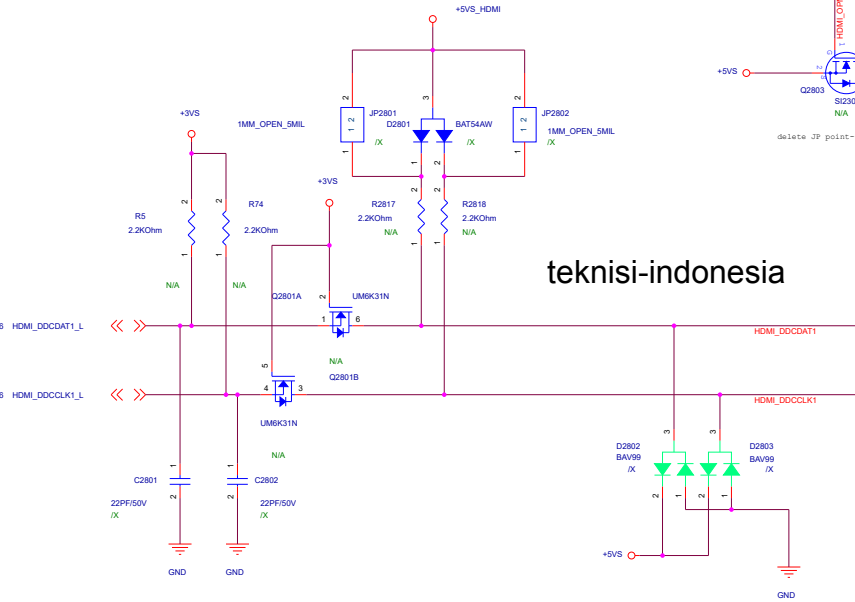


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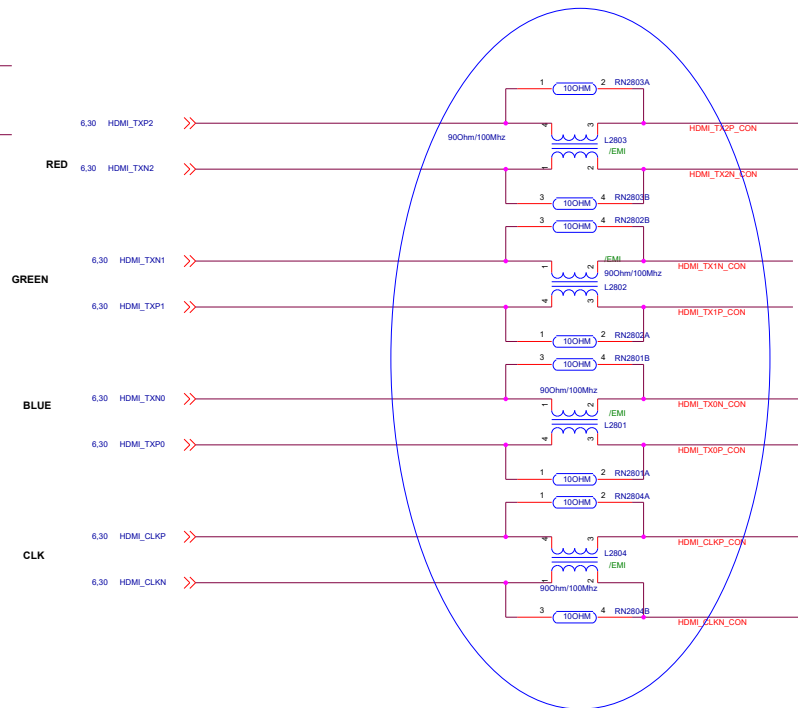
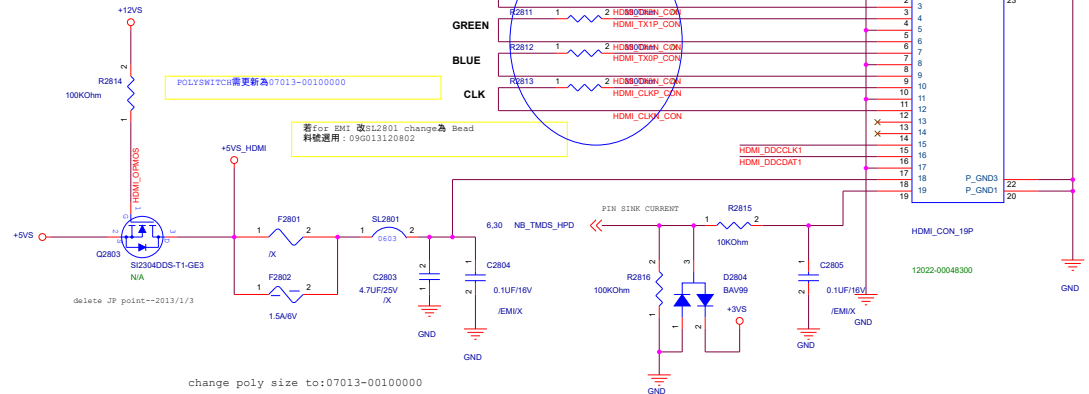
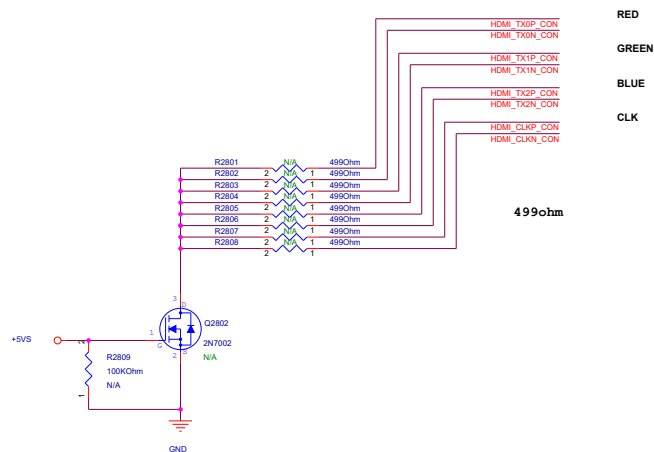
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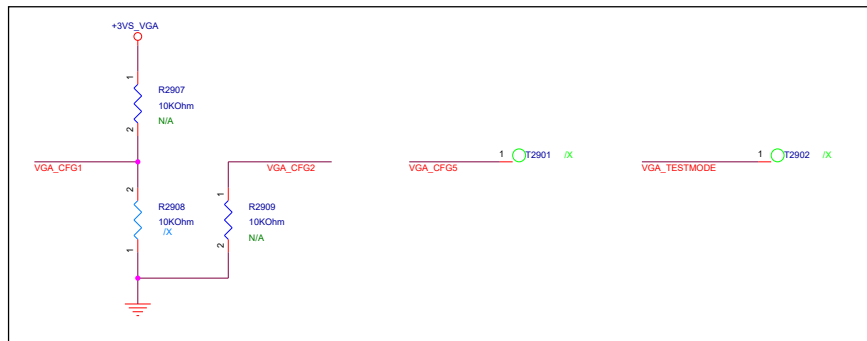
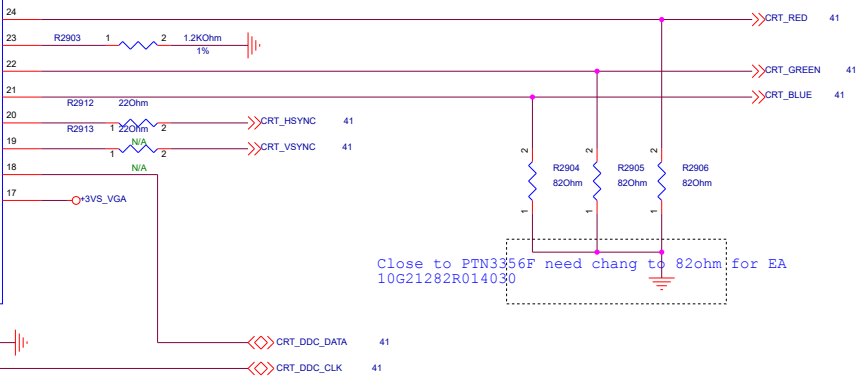
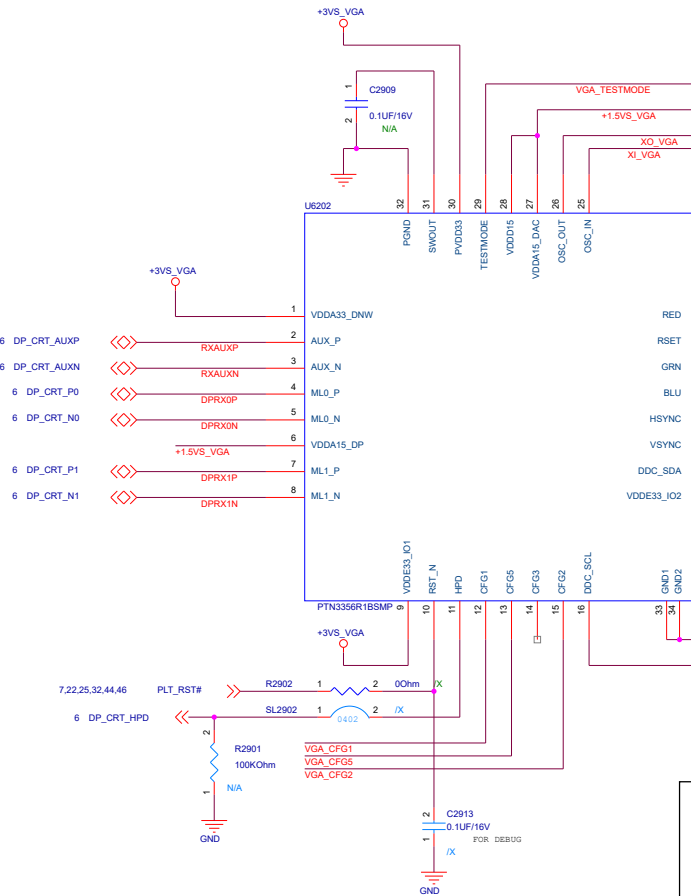
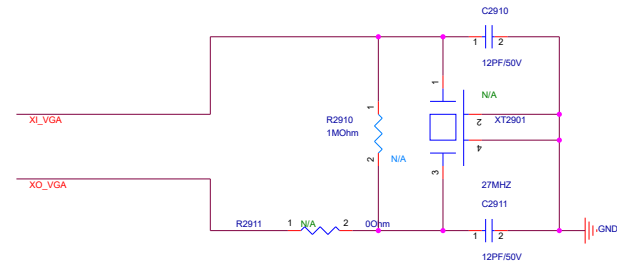
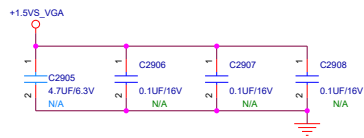
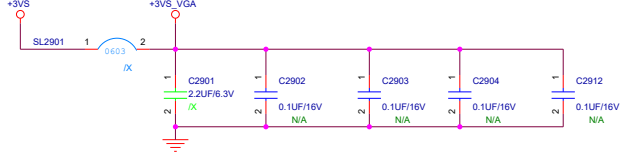
Del HDMI ESD component



teknisi-indonesia

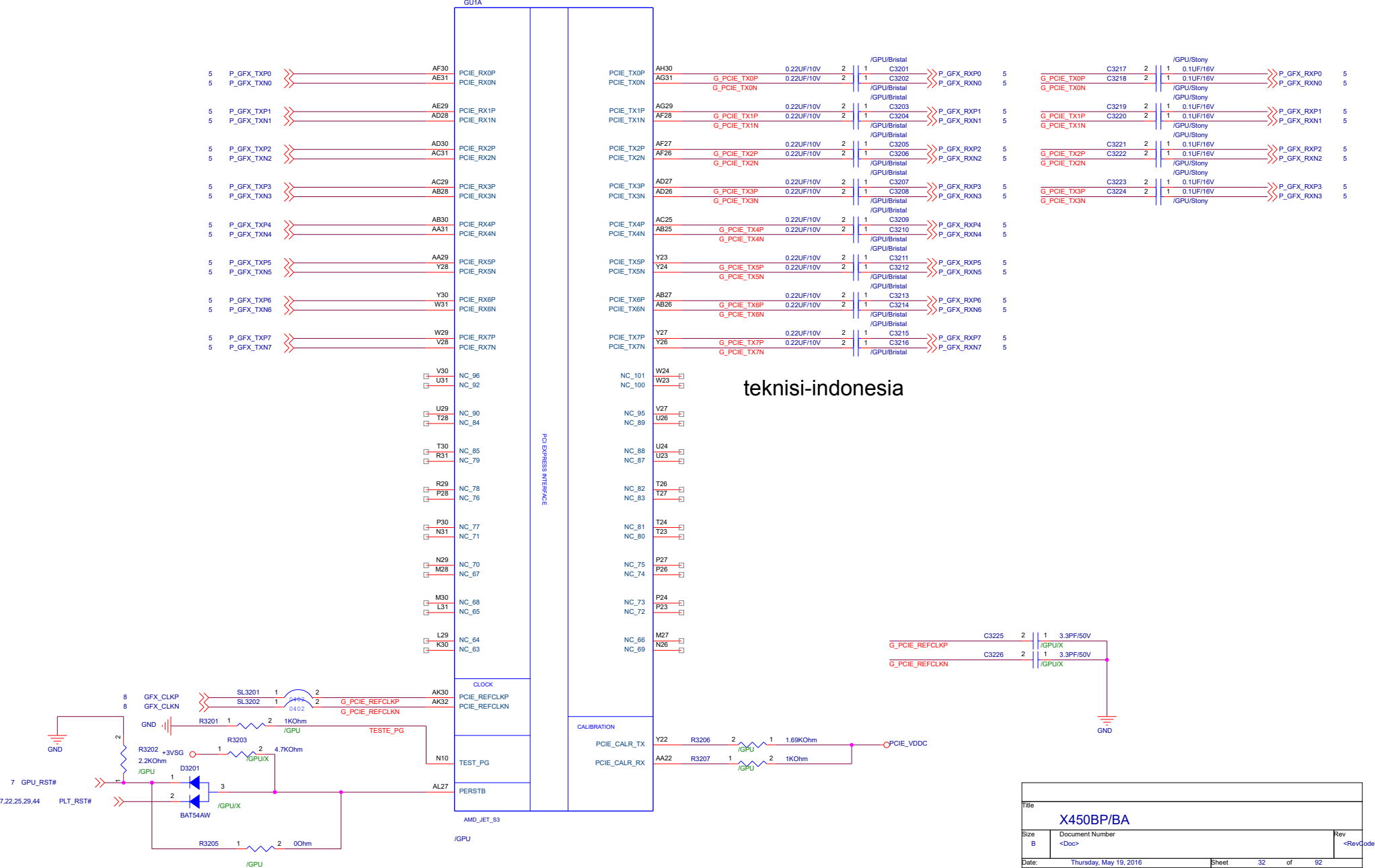


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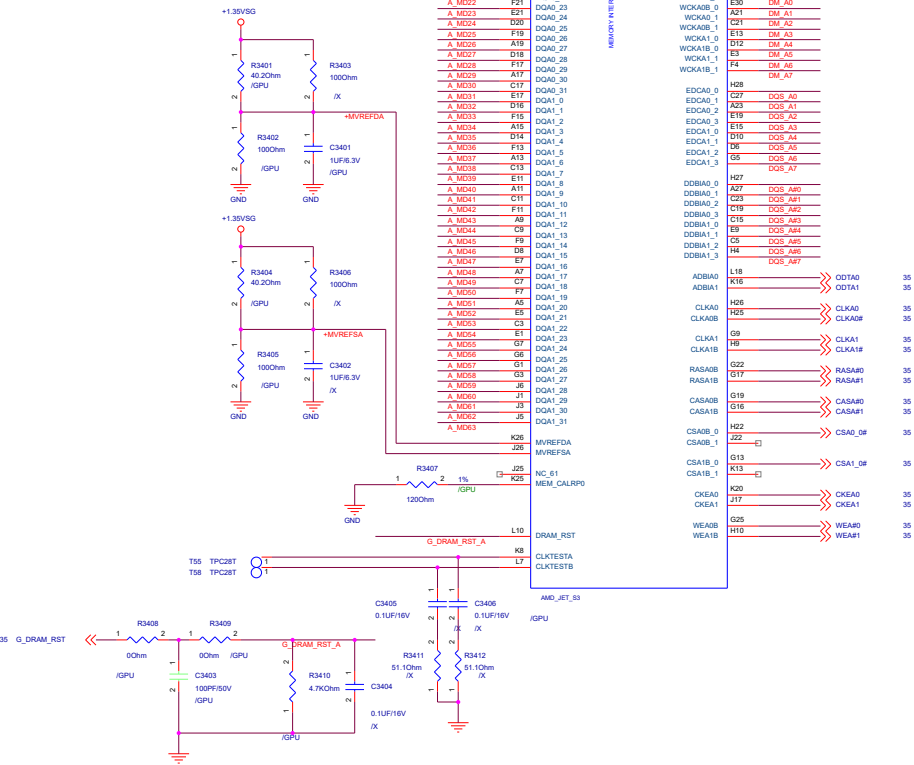


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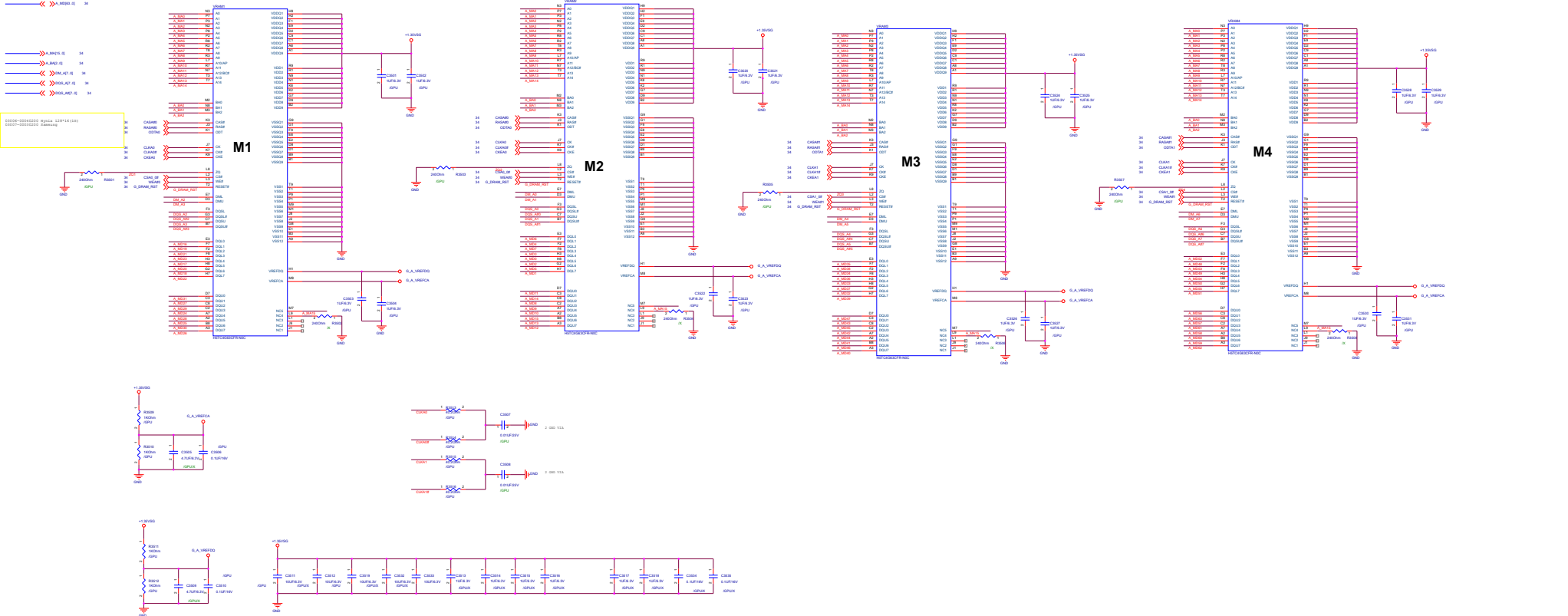
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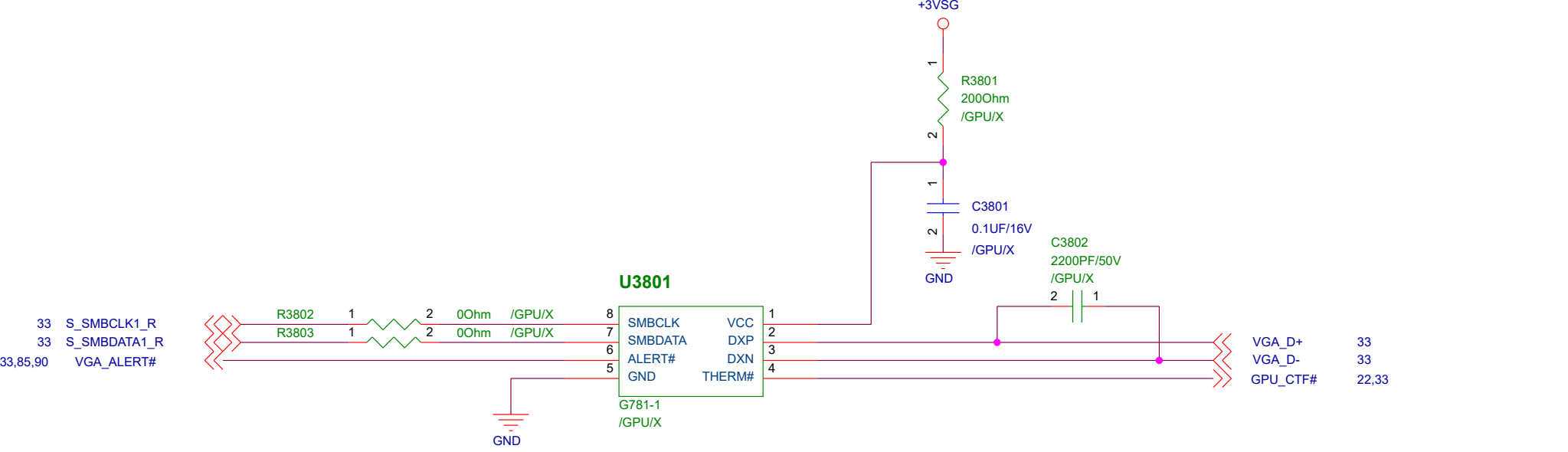


<< A_MA15_0 35
<< A_BA2_0 35
<< DM_A17_0 35
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35 G_DRAM_RST

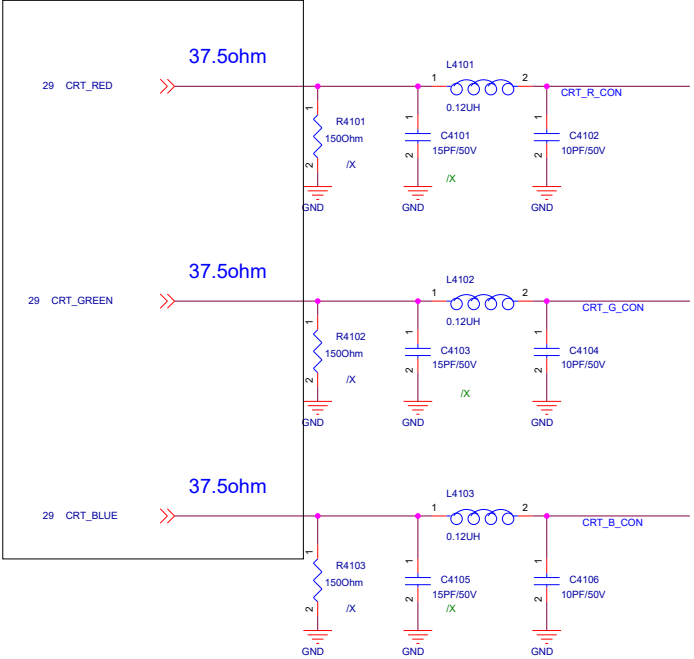




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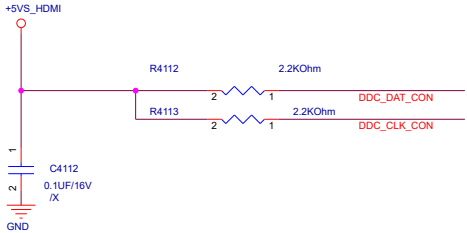
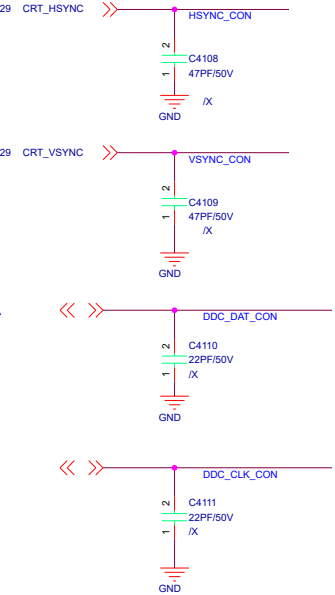
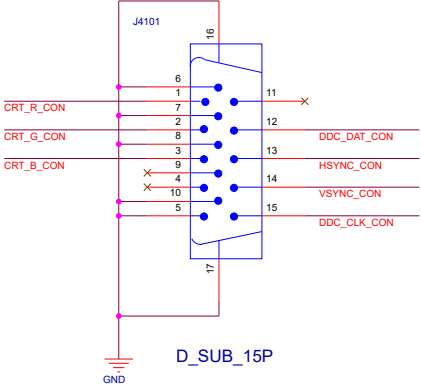
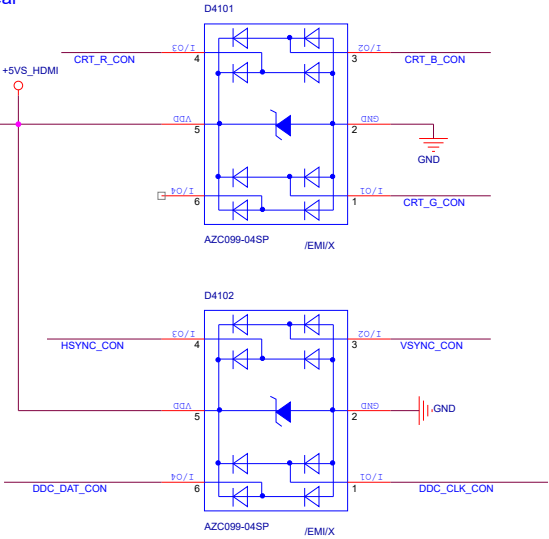
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L4101,L4102,L4103 change to poll :09G023122306,09G023122310

PLACE ESD
Diodes near
VGA port



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Speaker

INTERNAL MICROPHONE

AUDIO JACK

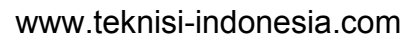
Apple & NOKIA & HP & MIC

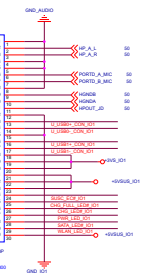
SDWP: Internal Pull-up
SDCDN: Internal
Pull-up

SDWP = 1 Write protect
SDWP = 0 Write-able
SDCDN = 1 No card
SDCDN = 0 Card
inserted

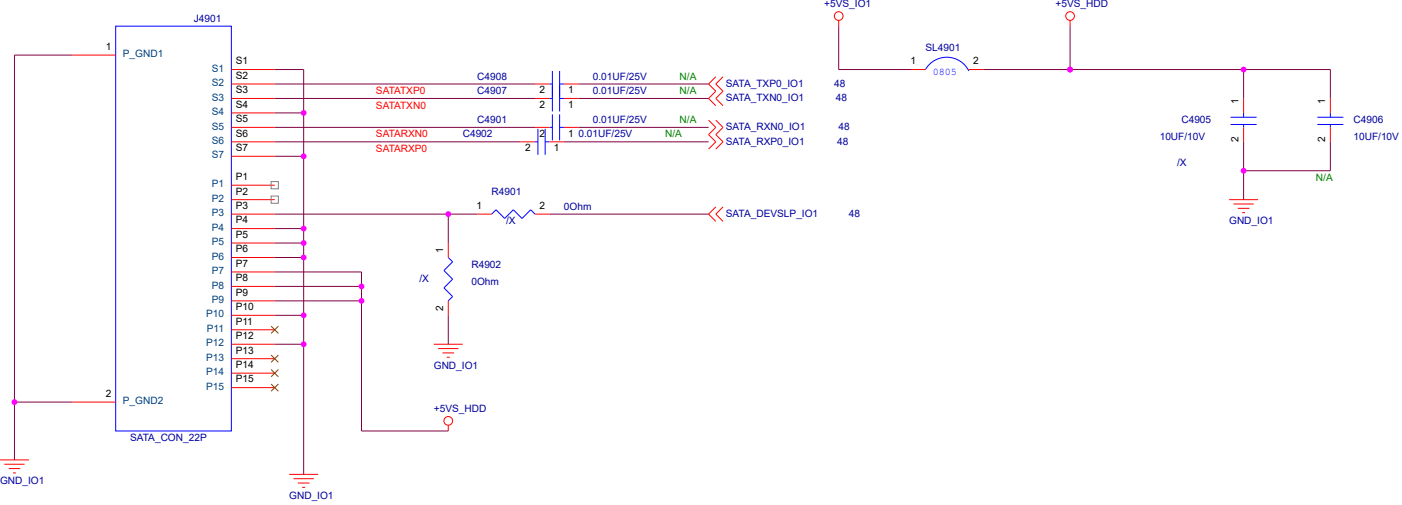
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X450BP/BA			
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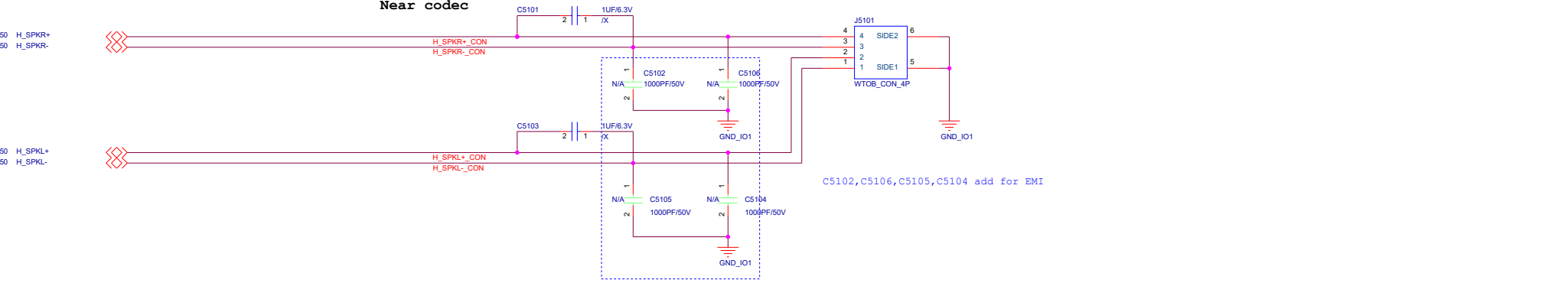
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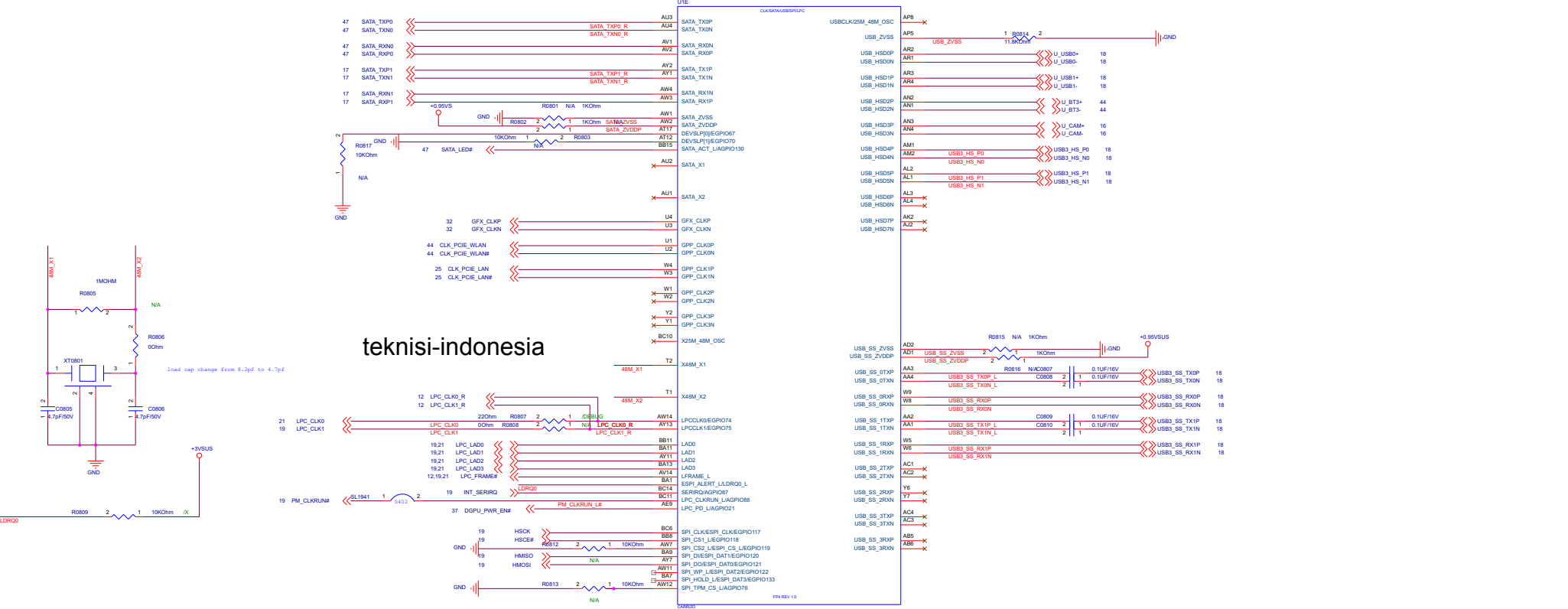


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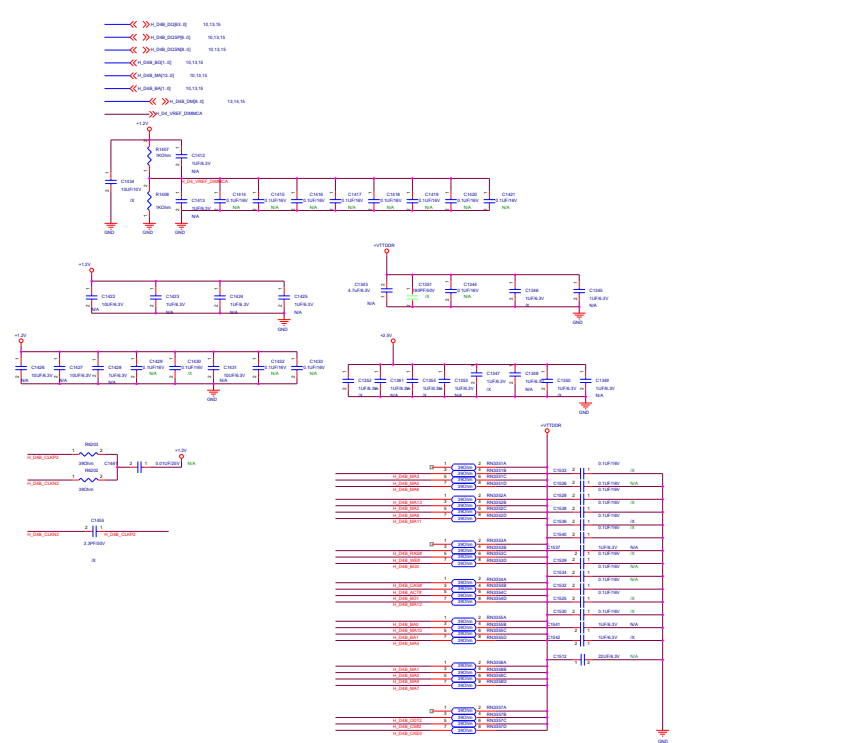


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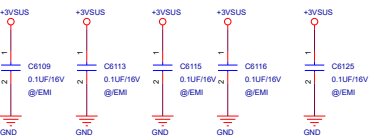
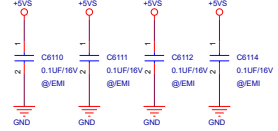
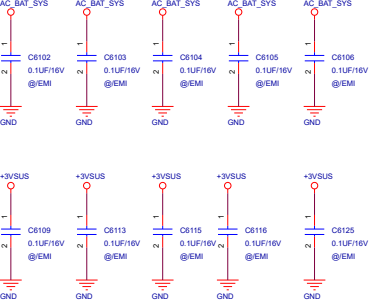
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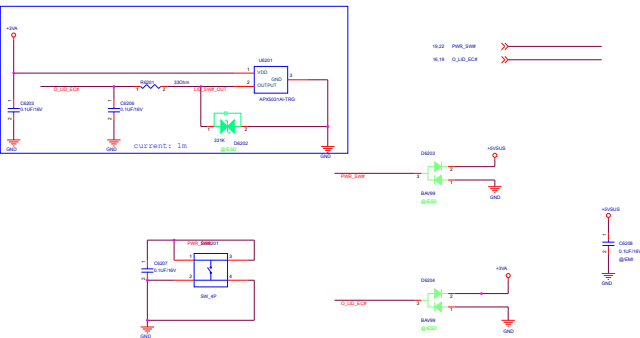
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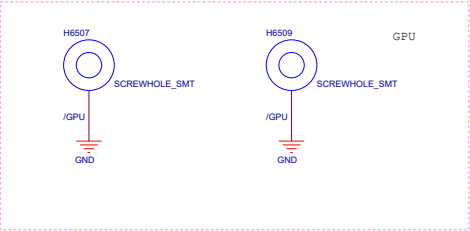
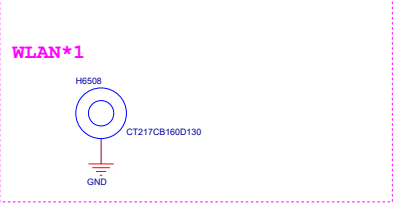
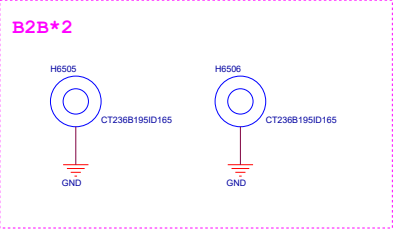
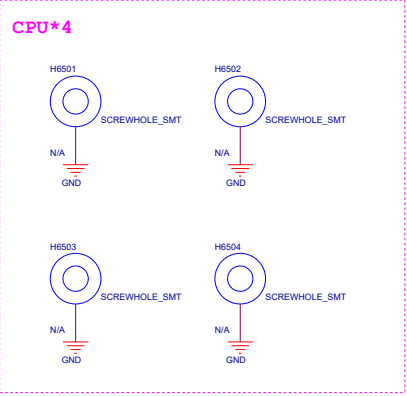
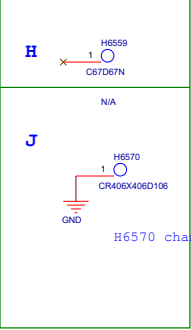
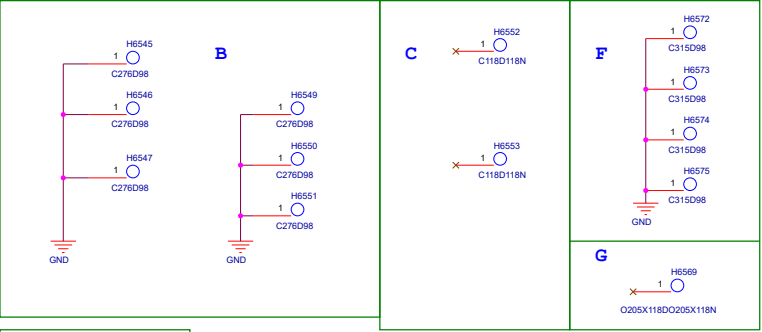
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ASUSTeK COMPUTER INC.

Engineer: **SZ_SYS2**

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C	X550	1.0

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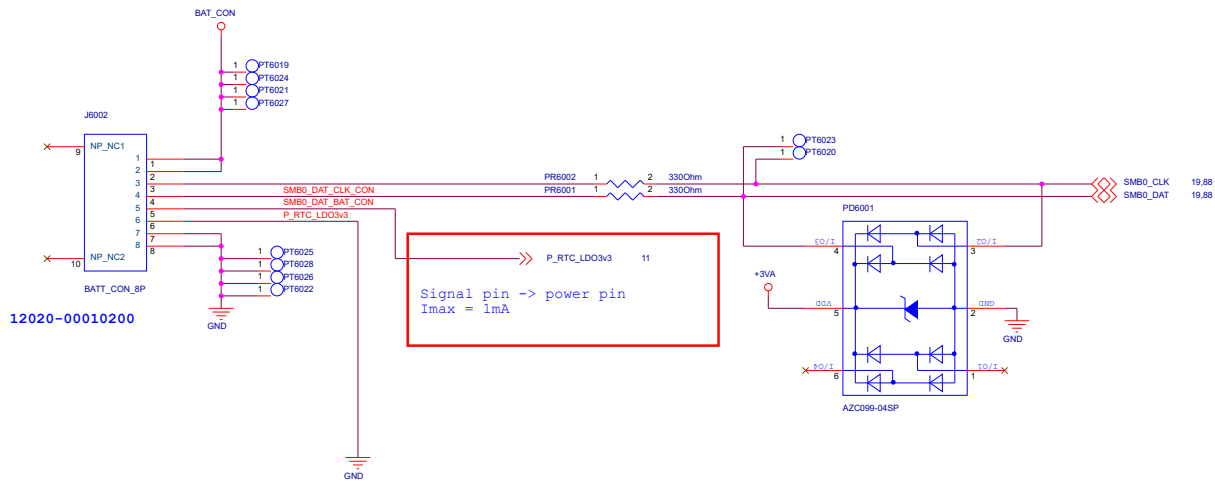
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Battery Connector



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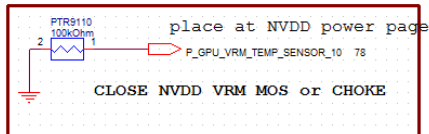
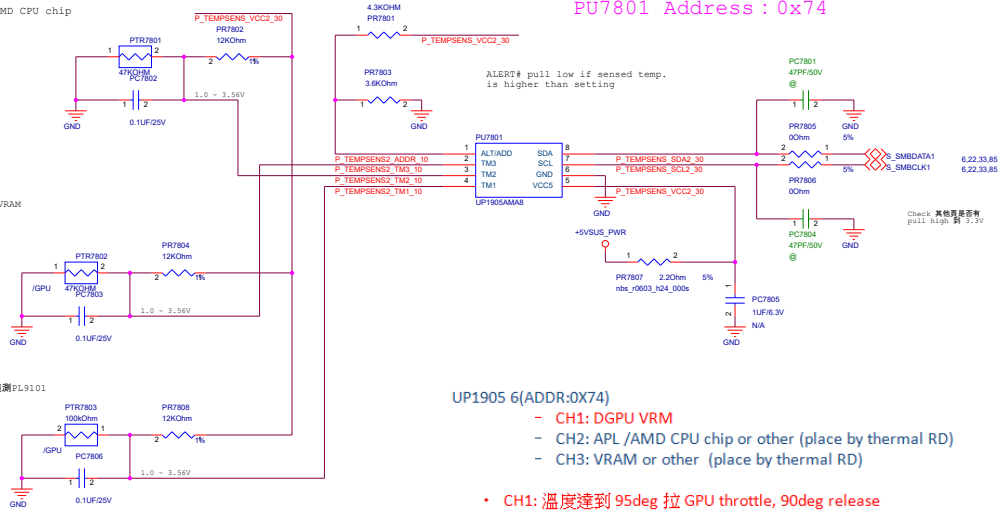
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PTR7802 place near 偵測DGPU VRAM

PTR7803 place near 偵測PL9101







For UPI DLPM




	Bristol 35w	Stony + Bristol	15w
	1.05V	0.95V	
PRFB8304	10KOhm 10G212100214010	12KOHM 10G212120214030	
PRFB8303	4.02KOHM 10G212402114030	3.24KOHM 10G212324114010	

P_1.0VSUS_DL_30	1	PT8303	NB_TPC20T
P_1.0VSUS_PHASE_S	1	PT8302	NB_TPC20T
P_1.0VSUS_DH_30	1	PT8301	NB_TPC20T

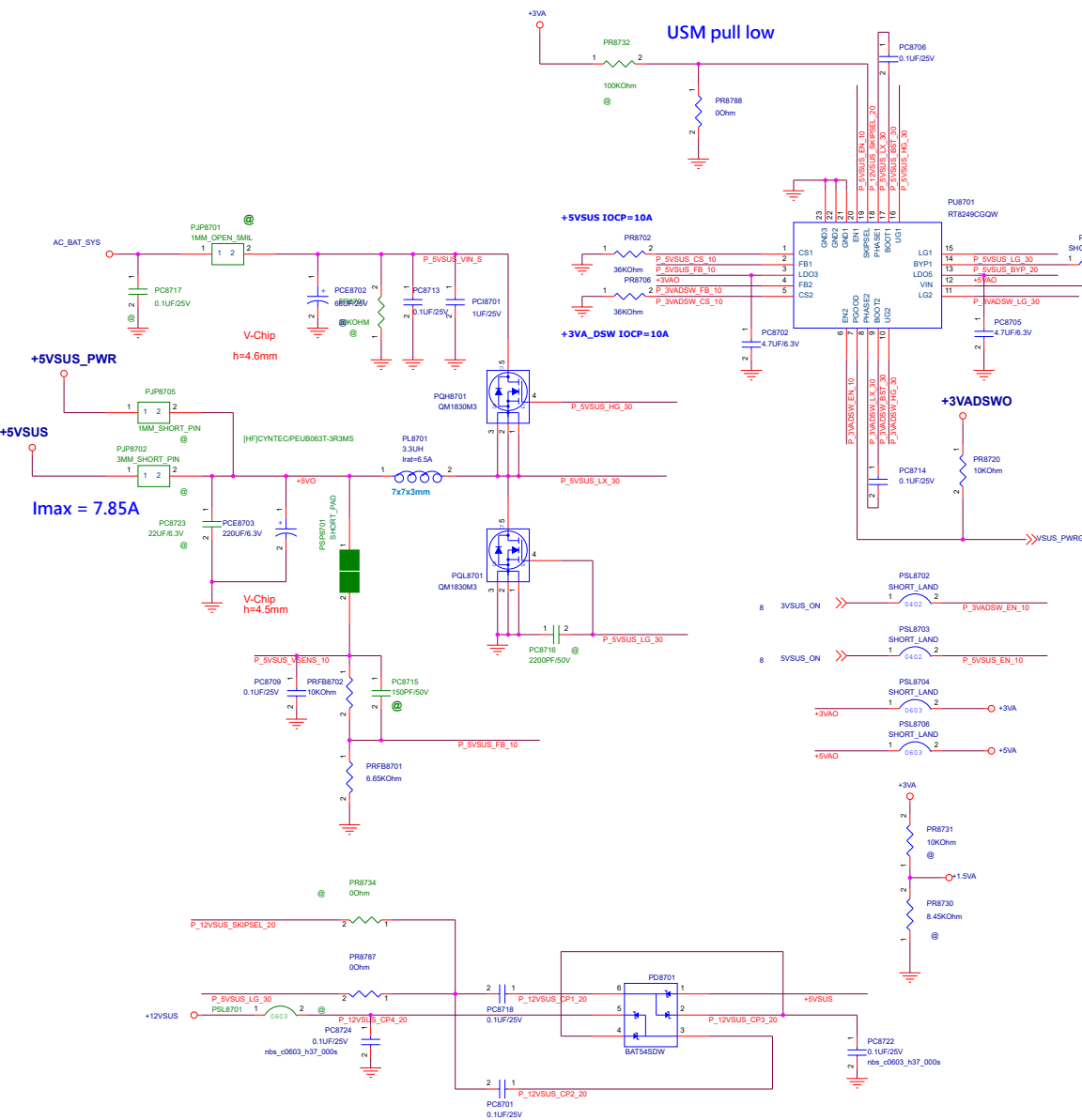
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ASUSTeK COMPUTER INC. NB1		Engineer: Day	
Size	Project Name		Rev
A	CZ STD		R1.0
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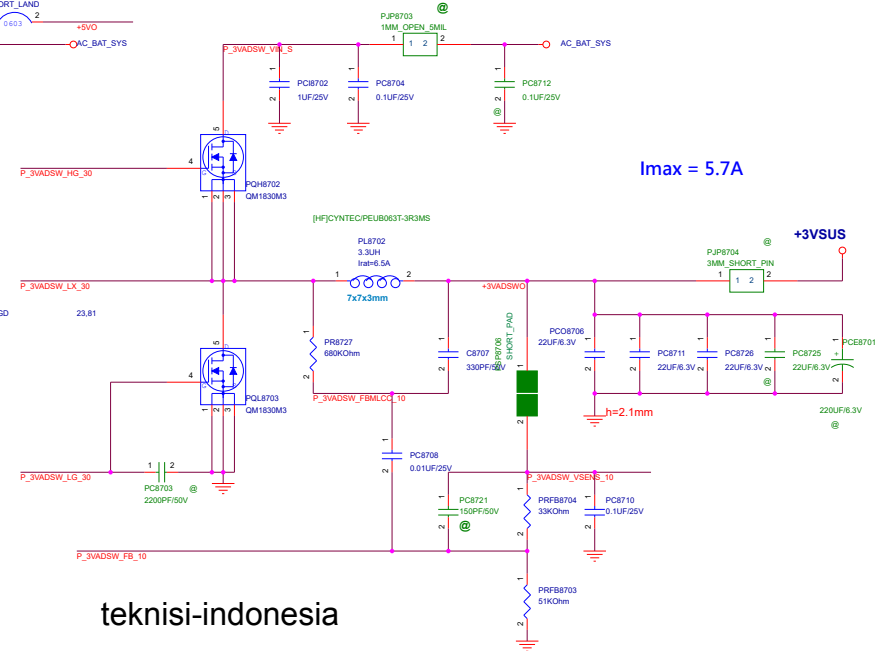


+3VA_DSW / +5VSUS [System Power]



Now Use This One

Part Reference	RT8249CQW		UP9003QQKF	
	MLCC	V-CHIP	MLCC	V-CHIP
PC8707	330pF	@	150pF	@
PR8727	680Kohm	@	680Kohm	@
PC8708	0.01uF	@	0.01uF	@



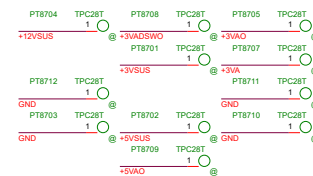
teknisi-indonesia

Adaptor Mode (IMVP8)

	S0	CS	S3	DS3	S4	S5	S5 with USB Charger*
PS_ON	1	-	1	-	1	-	1
3VADSW_ON	1	-	1	-	1	-	1
5VSUS_ON	1	-	1	-	0	-	0
5VSUS_ON	1	-	1	-	1	-	1
1.35V_ON	1	-	1	-	0	-	0
SUSC_EC#	1	-	1	-	0	-	0
SUSB_EC#	1	-	0	-	0	-	0

Battery Mode (IMVP8)

	S0	CS	S3	DS3	S4	S5	S5 with USB Charger*
PS_ON	1	-	-	1	0	0	1
3VADSW_ON	1	-	-	1	0	0	0
5VSUS_ON	1	-	-	0	0	0	0
5VSUS_ON	1	-	-	1	0	0	1
1.35V_ON	1	-	-	1	0	0	0
SUSC_EC#	1	-	-	0	0	0	0
SUSB_EC#	1	-	-	0	0	0	0

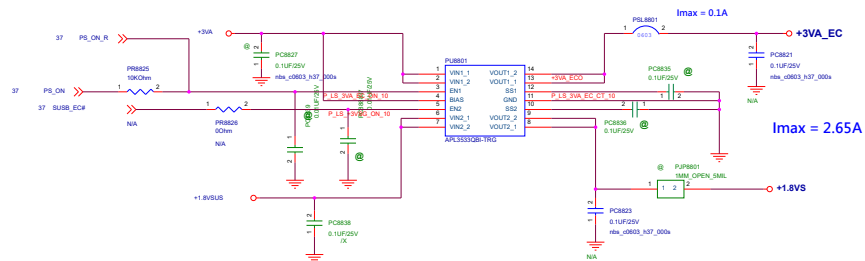
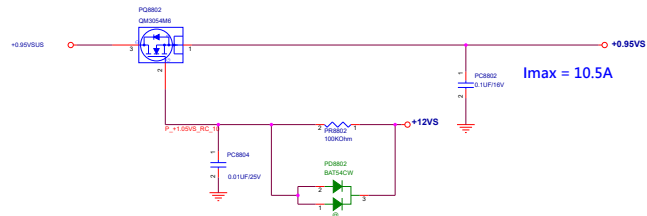
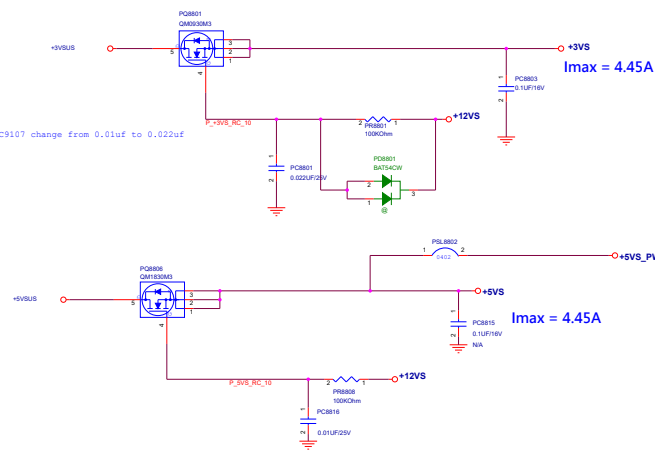


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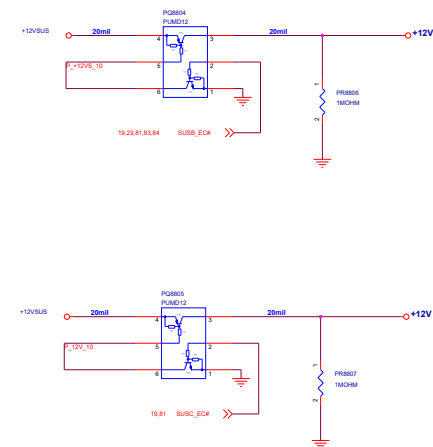
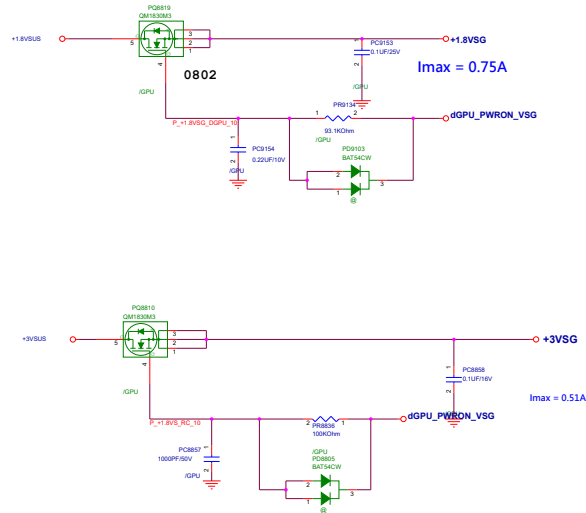
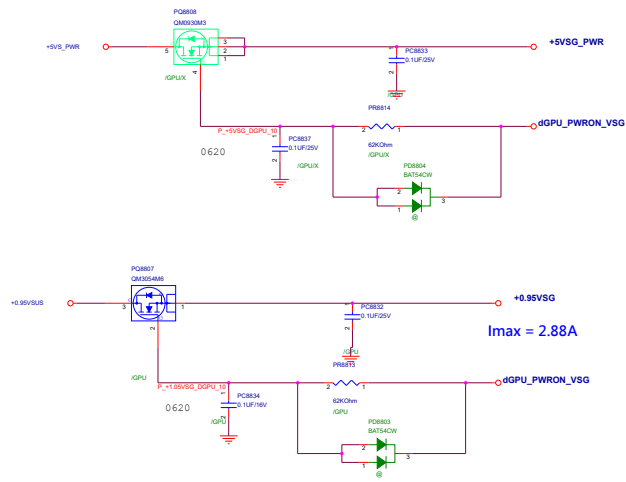
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KX STD		NB Power Team		Power
Title : PW +3VA_DSW/+5VSUS		Date: Thursday, May 10, 2016		Sheet 67 of 99

+1.5V ,+1.5VS,+0.95VS,1.8VS,+3VS,+5VS

ER PC9103 PC9107 change from 0.01uf to 0.022uf

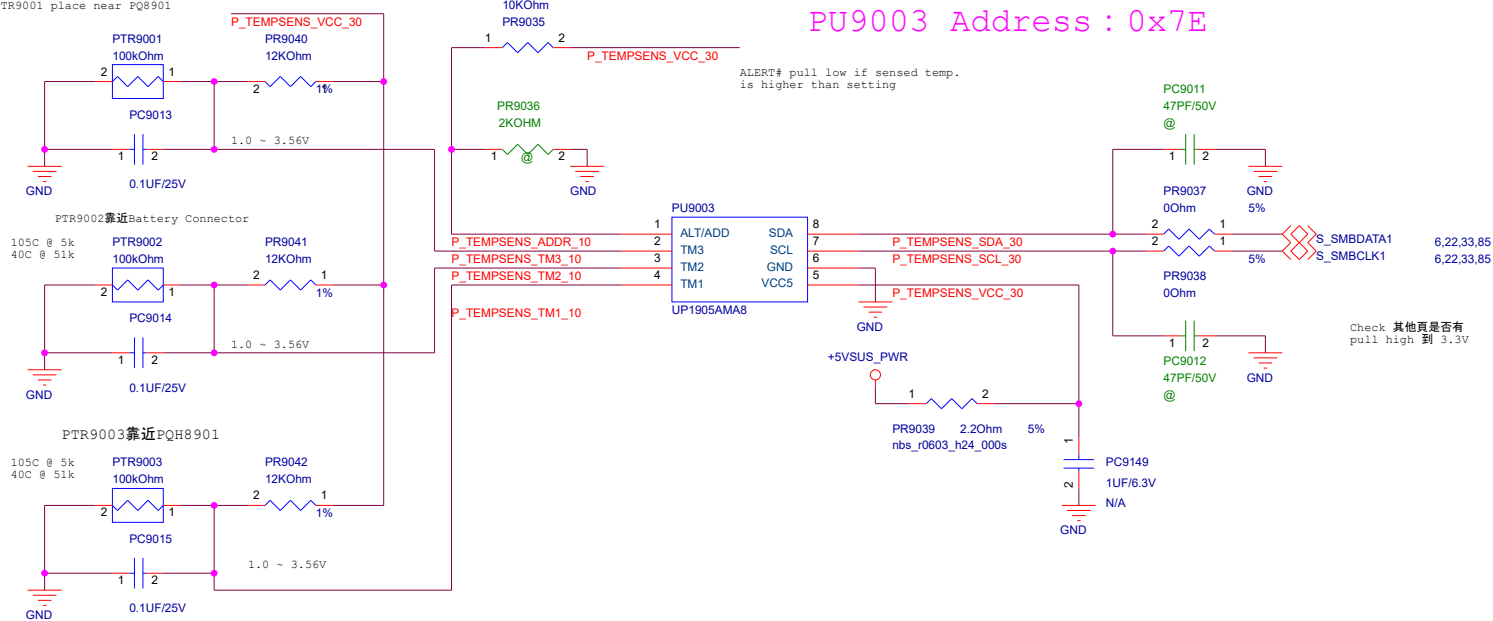


以下部份，在有DGPU的時候上件



PTR9001 place near PQ8901

PU9003 Address : 0x7E

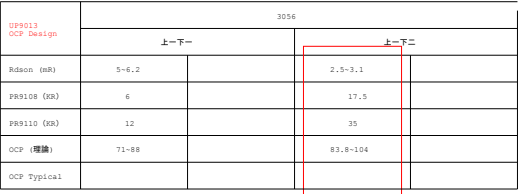


UP1905 1(ADDR:0X7E)

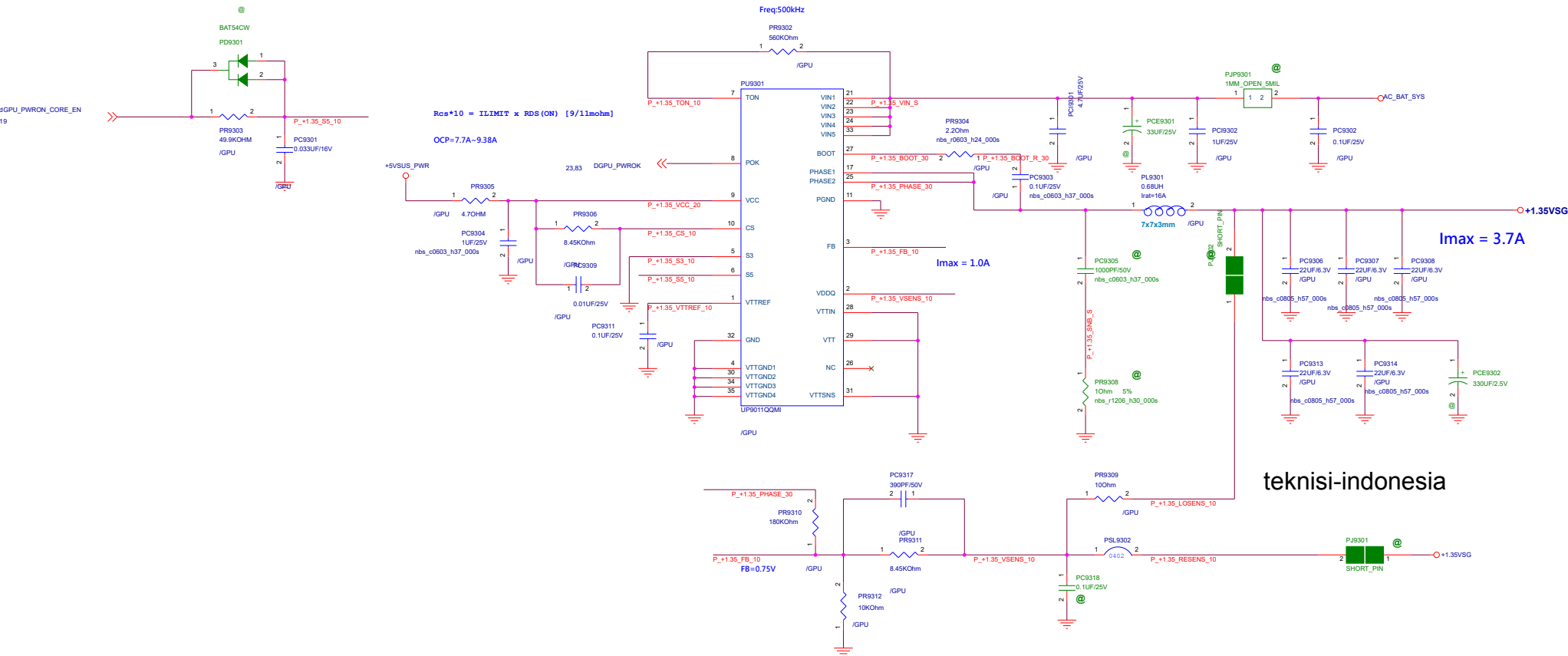
- CH1: Charger MOS or Choke
- CH2: BAT connector
- CH3: ACFET
- CH1: 溫度達到 95deg stop charging/charging boost , 85deg release
- CH2: 溫度達到 95deg 拉 CPU/GPU throttle, 90deg release, 110deg 進 S4
- CH3: 溫度達到 95deg 拉 CPU/GPU throttle, 90deg release, 110deg 進 S4
-

<Variant Name>

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PW_+1.35VSG(UP9011)




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ASUSTeK COMPUTER INC. NB1		Engineer: Day	
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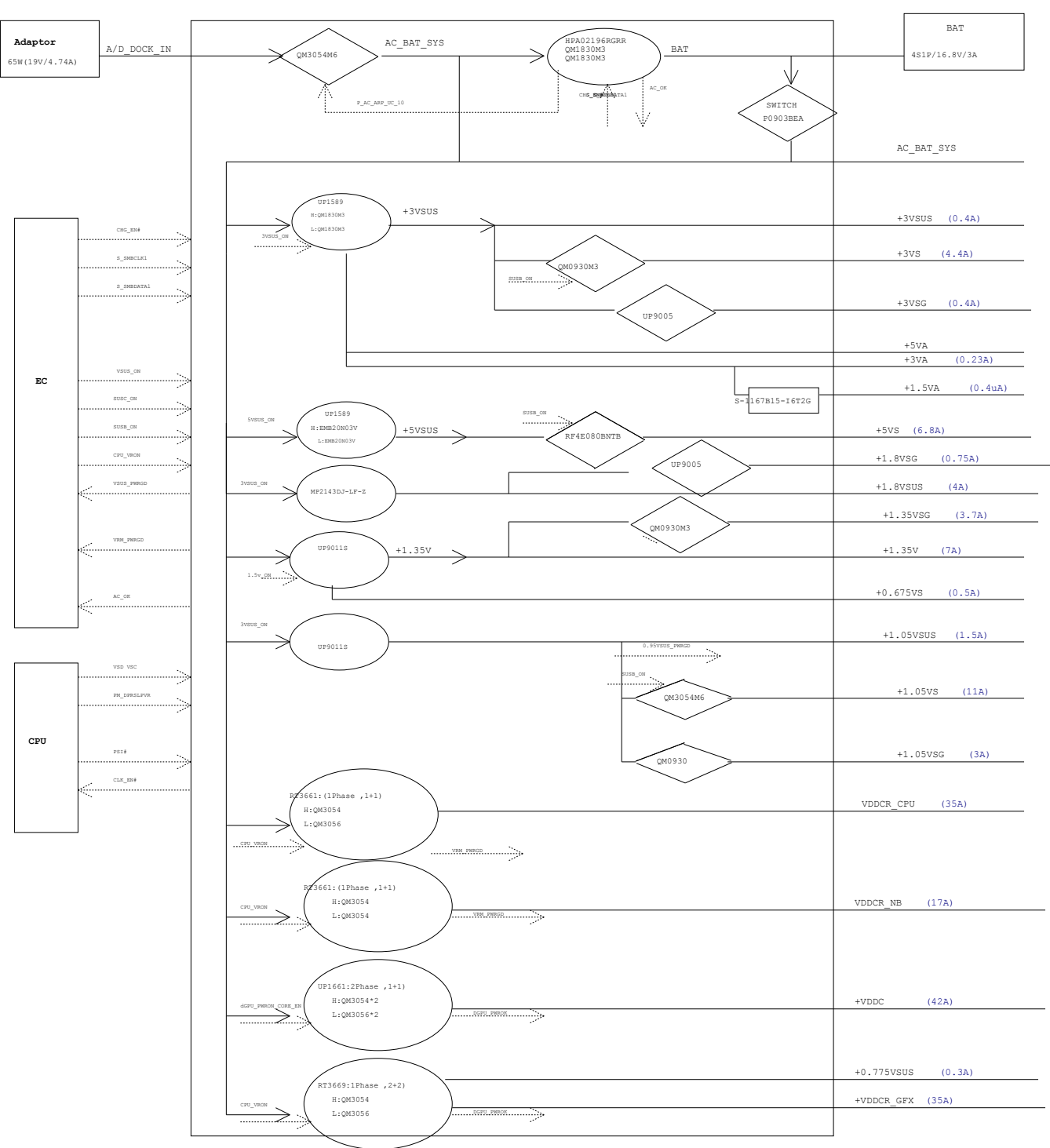
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ASUSTeK COMPUTER INC. NB1		Engineer: Day	
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ASUSTeK COMPUTER INC. NB1		Engineer: Day	
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Version Name: